

Amendments to the Specification:

Please insert the following paragraph at page 20, line 11:
after
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IDC-A1,AMD

At step S710, the port control unit transmits the generated bandwidth output signal to the queue manager.

~~Please insert the following paragraph at page 23, line 12:~~
after
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IDC-A2,AMD

At step S904, the limit value detecting section 606 of the queue manager examines whether the address pointer of the packet memory is over the second limit value.